



FIG. 1

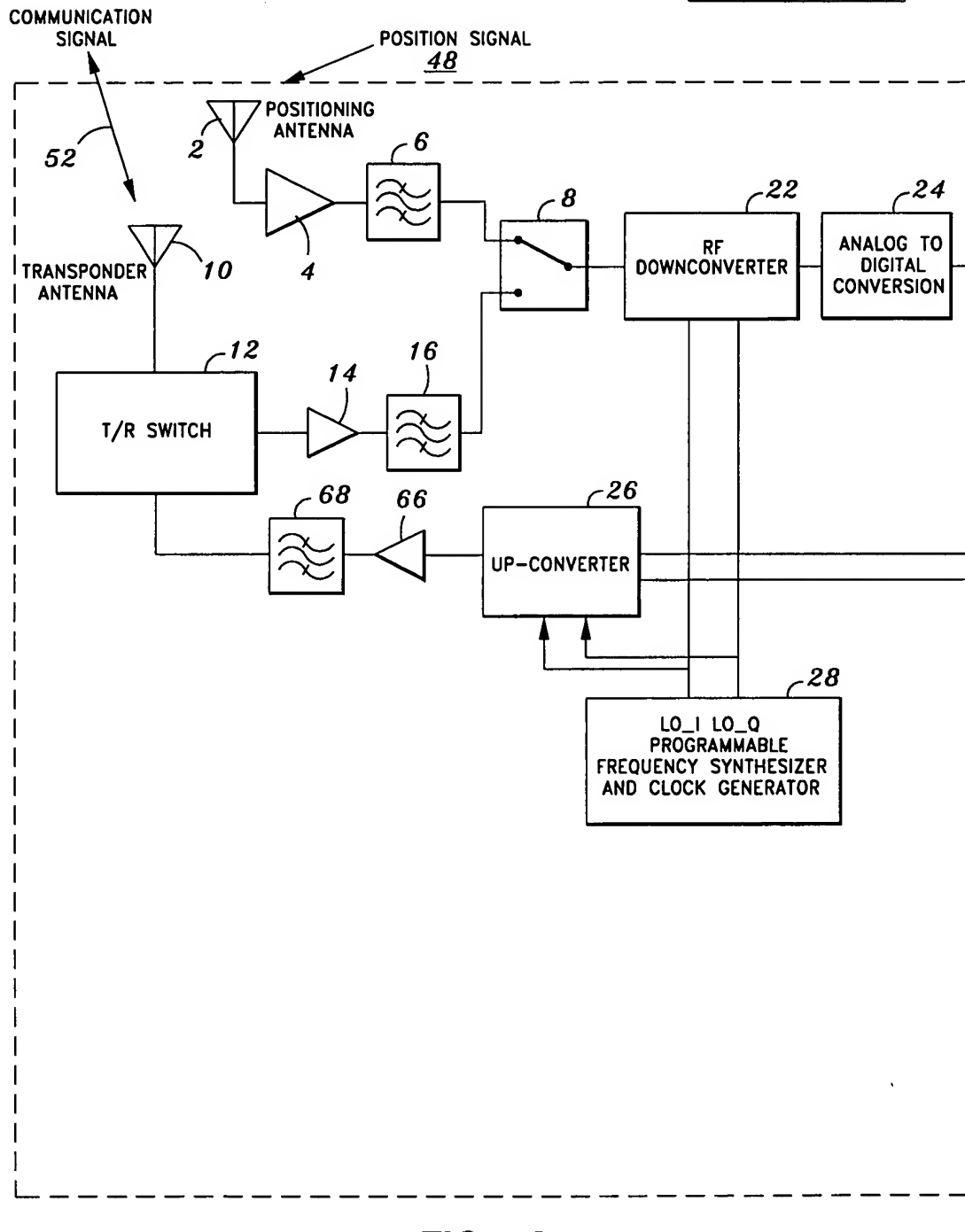
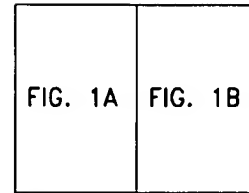


FIG. 1A

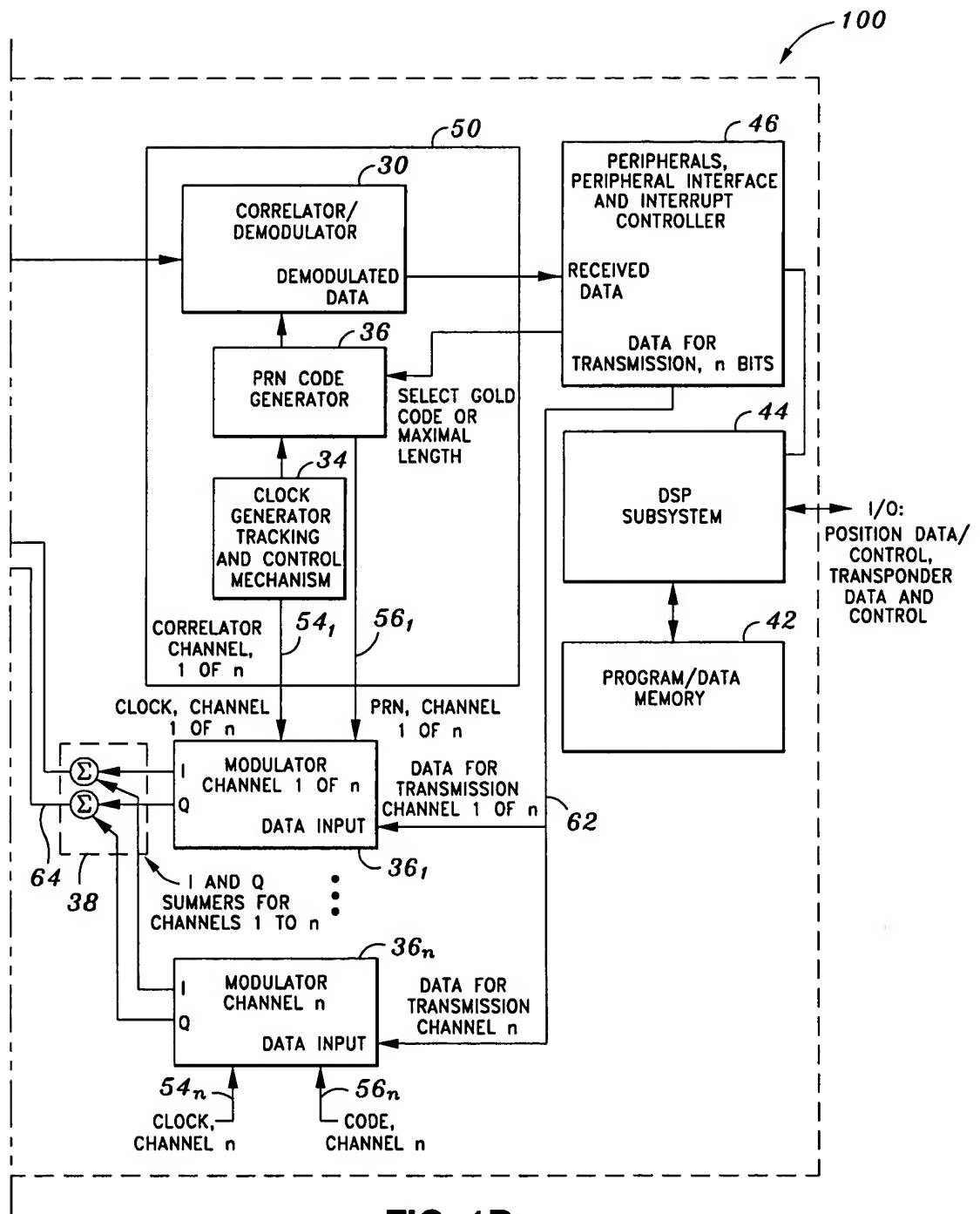


FIG. 1B

FIG. 2A1

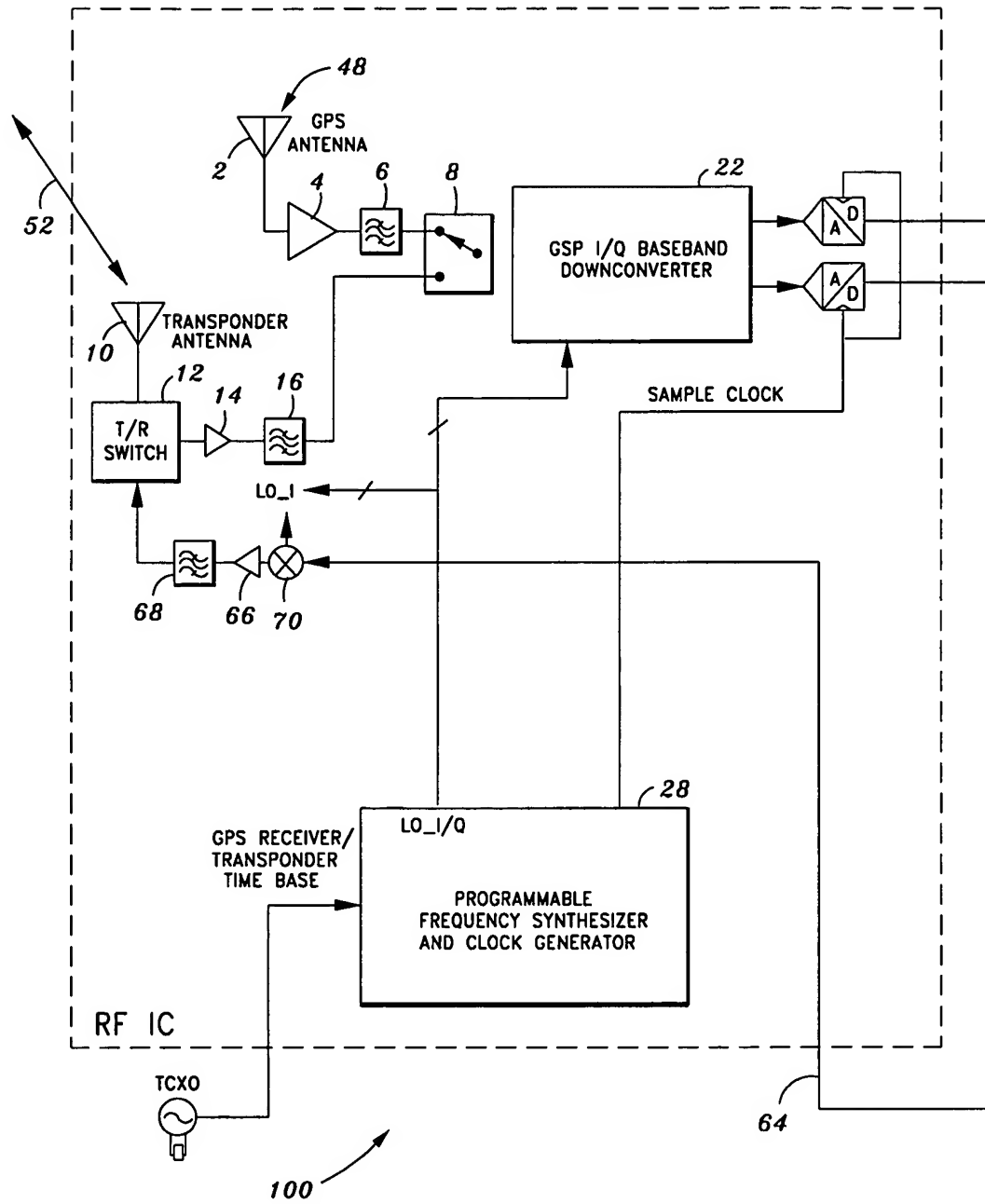
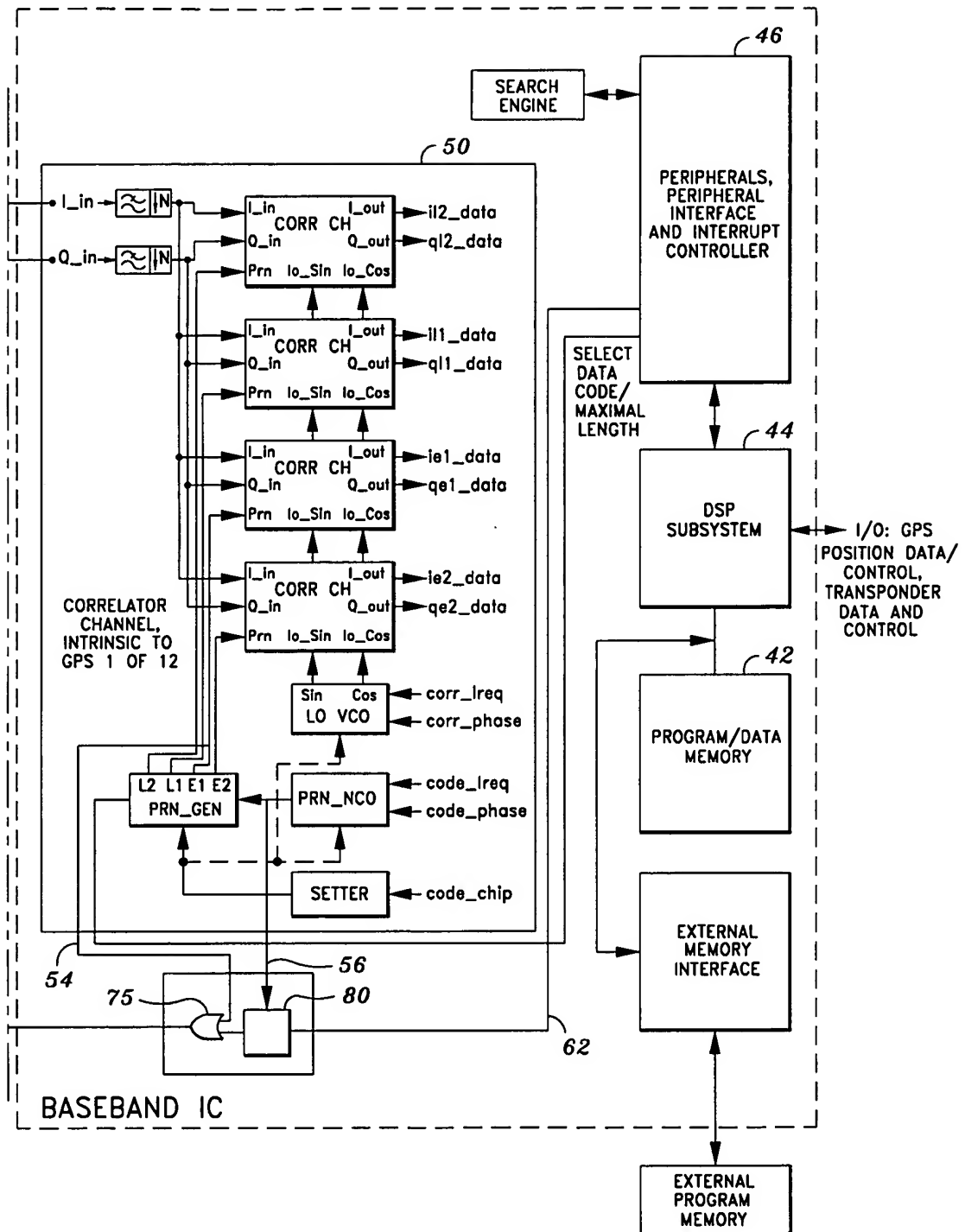


FIG. 2A

FIG. 2A1	FIG. 2A2
----------	----------

FIG. 2A2

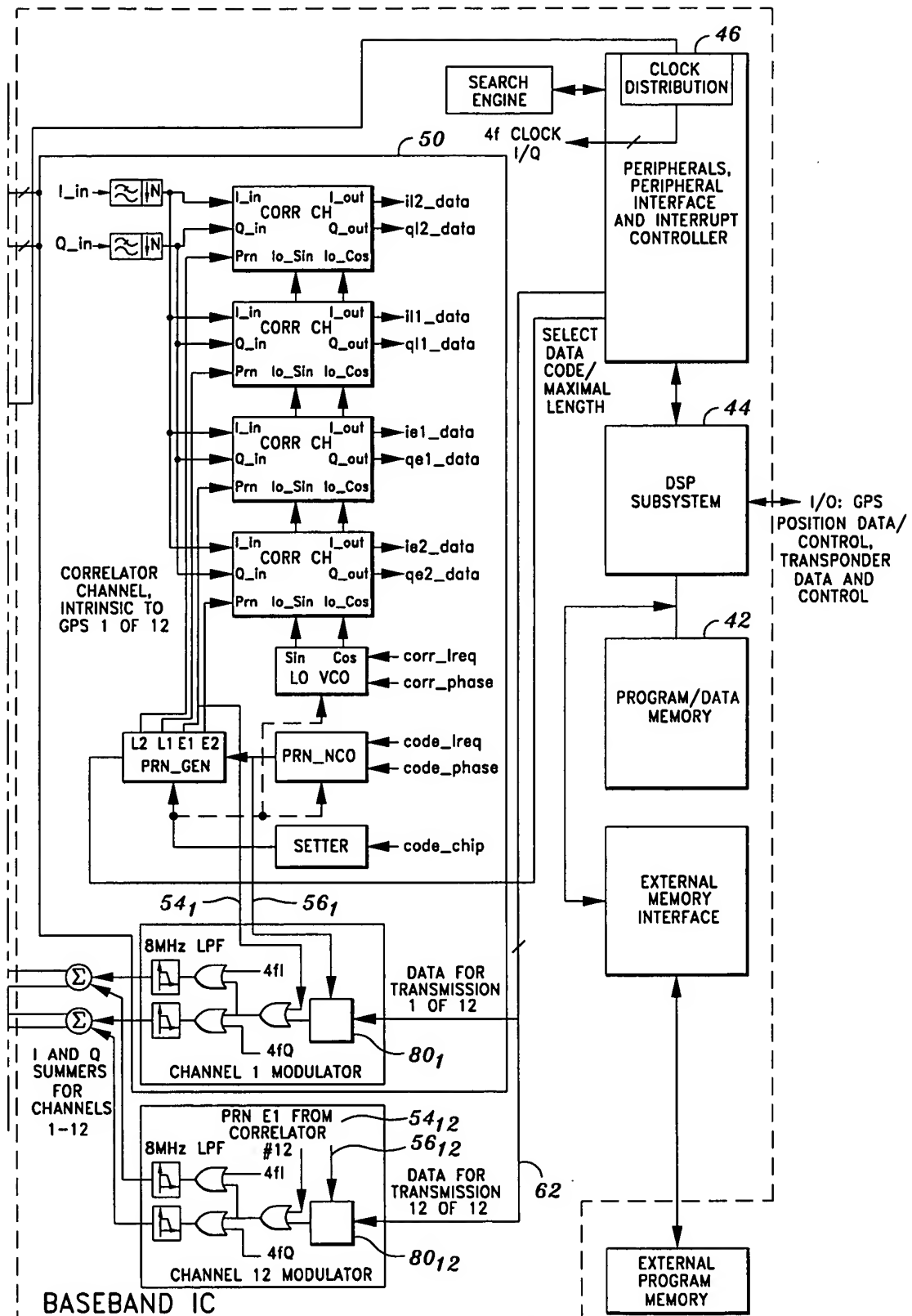


The diagram illustrates a GPS receiver/transponder system. It features a **GPS ANTENNA** (2) and a **TRANSPONDER ANTENNA** (10). The GPS antenna signal path includes a switch (4), a filter (6), and a switch (8) before entering the **GSP I/Q BASEBAND DOWNCONVERTER** (22). The transponder antenna signal path includes a **T/R SWITCH** (12), a filter (14), and a filter (16) before entering the same baseband converter. A **MULTI-CHANNEL CDMA Balun** (66) is connected to the T/R switch and a ground symbol. The baseband converter (22) outputs signals to two **A/D** converters, which are connected to a **SAMPLE CLOCK/MCLOCK** block. A **GPS RECEIVER/TRANSPONDER TIME BASE** block (28) provides a **PROGRAMMABLE FREQUENCY SYNTHESIZER AND CLOCK GENERATOR** (28), which outputs **LO_I** and **LO_Q** signals to the baseband converter and the sample clock block. The sample clock block also provides a **SAMPLE CLOCK/MCLOCK** signal to the baseband converter and the A/D converters. A **SSB UP-CONVERT** block (26) is also shown, receiving signals from the baseband converter and the sample clock block, and outputting signals to the multi-channel CDMA balun and the T/R switch.



FIG. 2B1	FIG. 2B2
----------	----------

FIG. 2B2



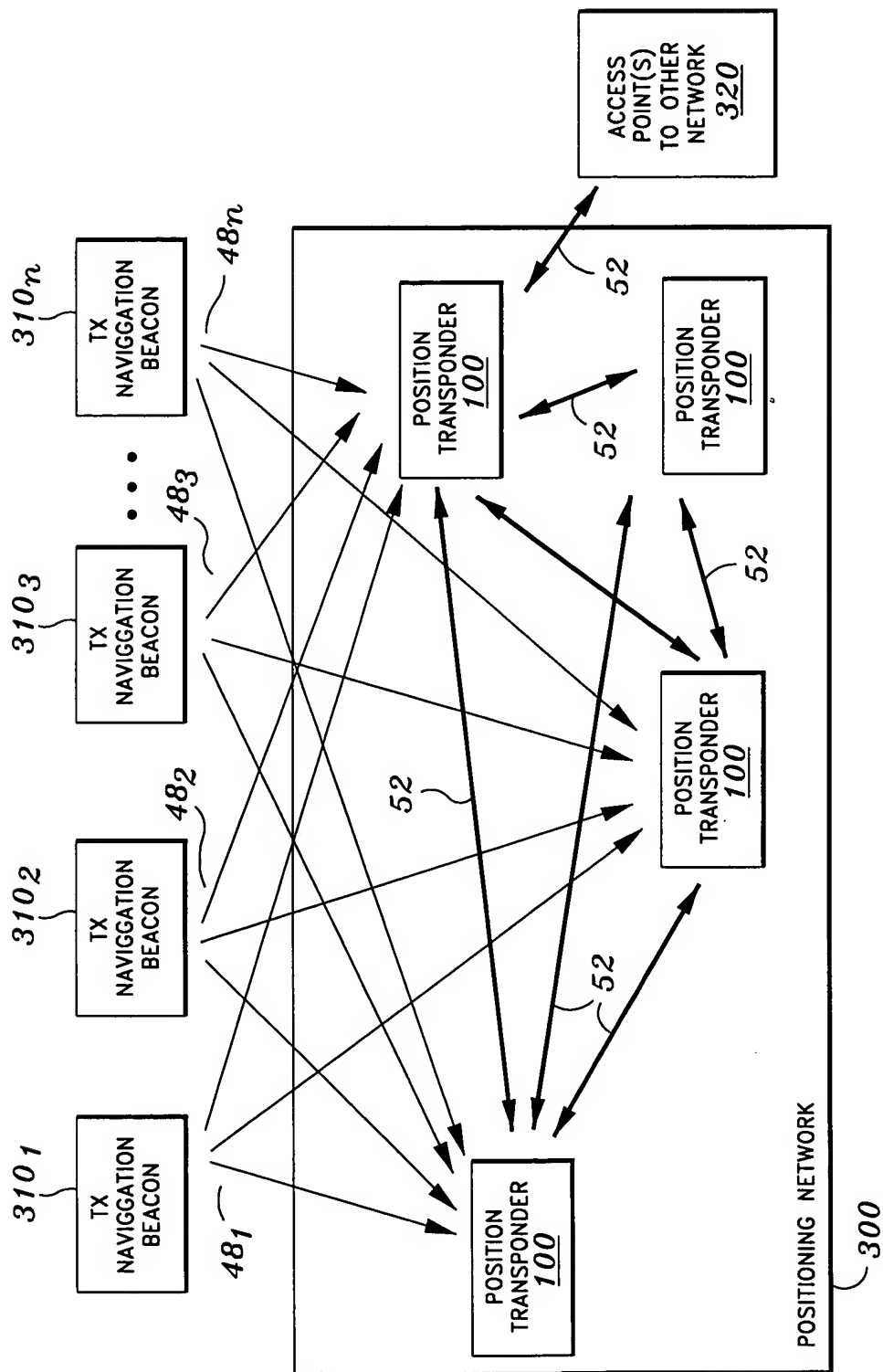


FIG. 3A

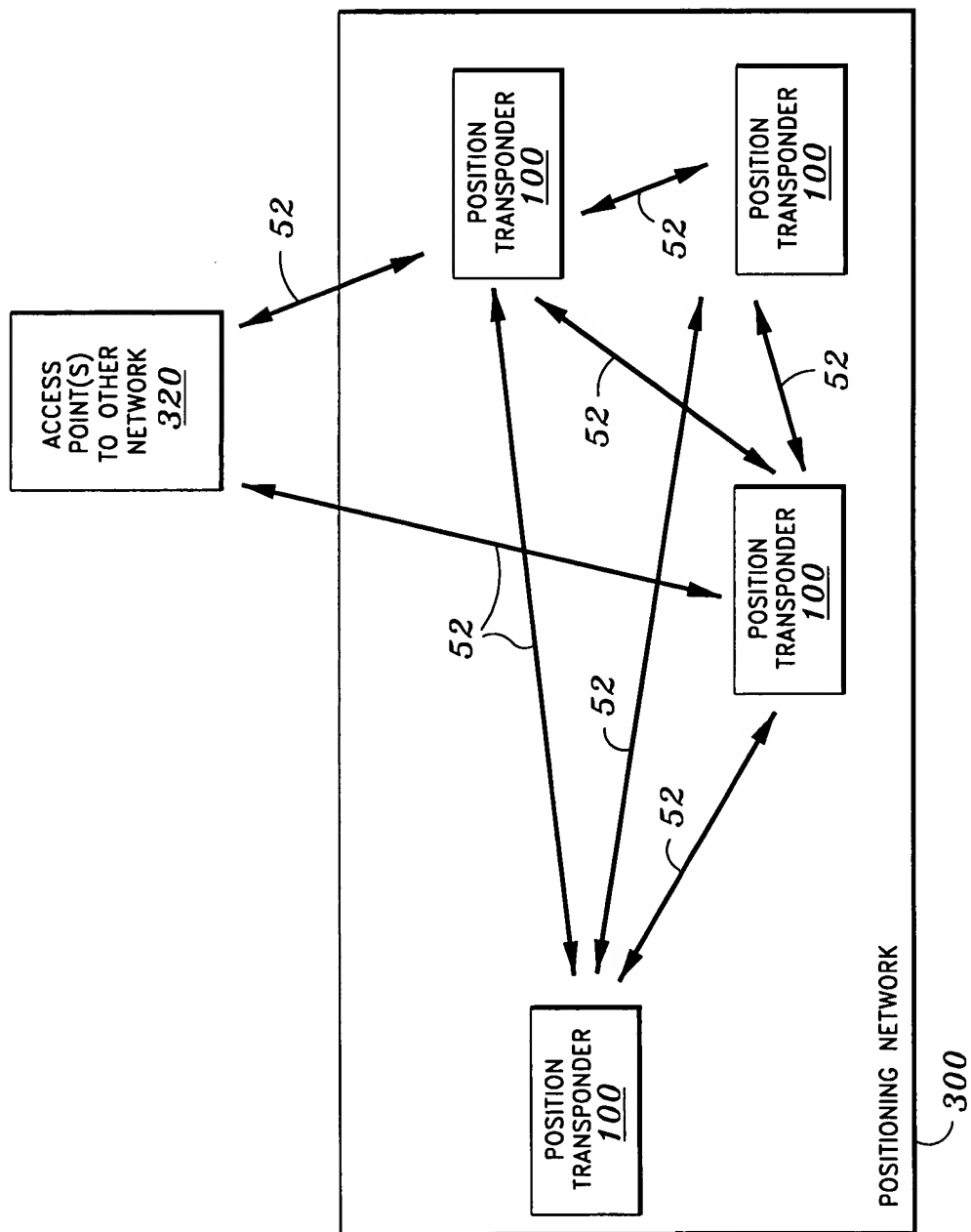


FIG. 3B

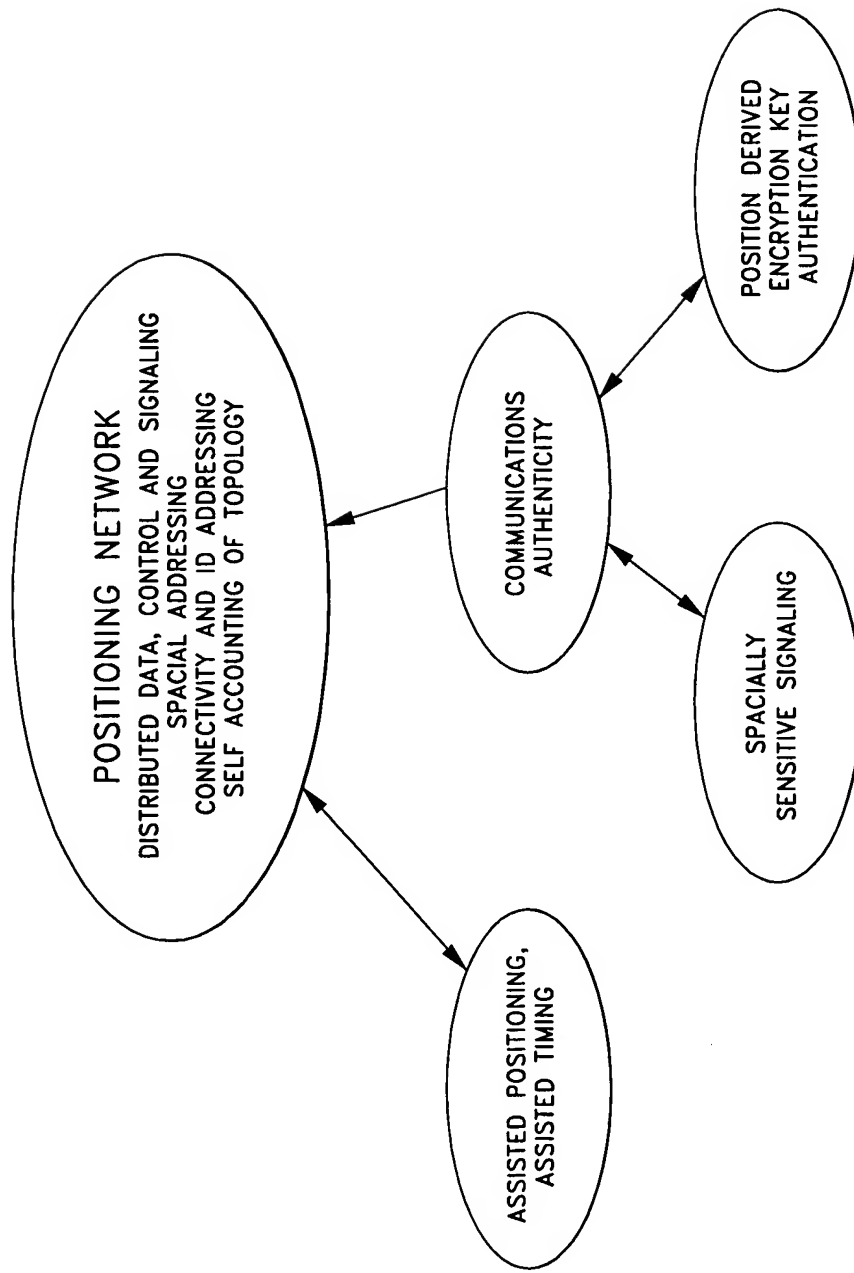


FIG. 4A

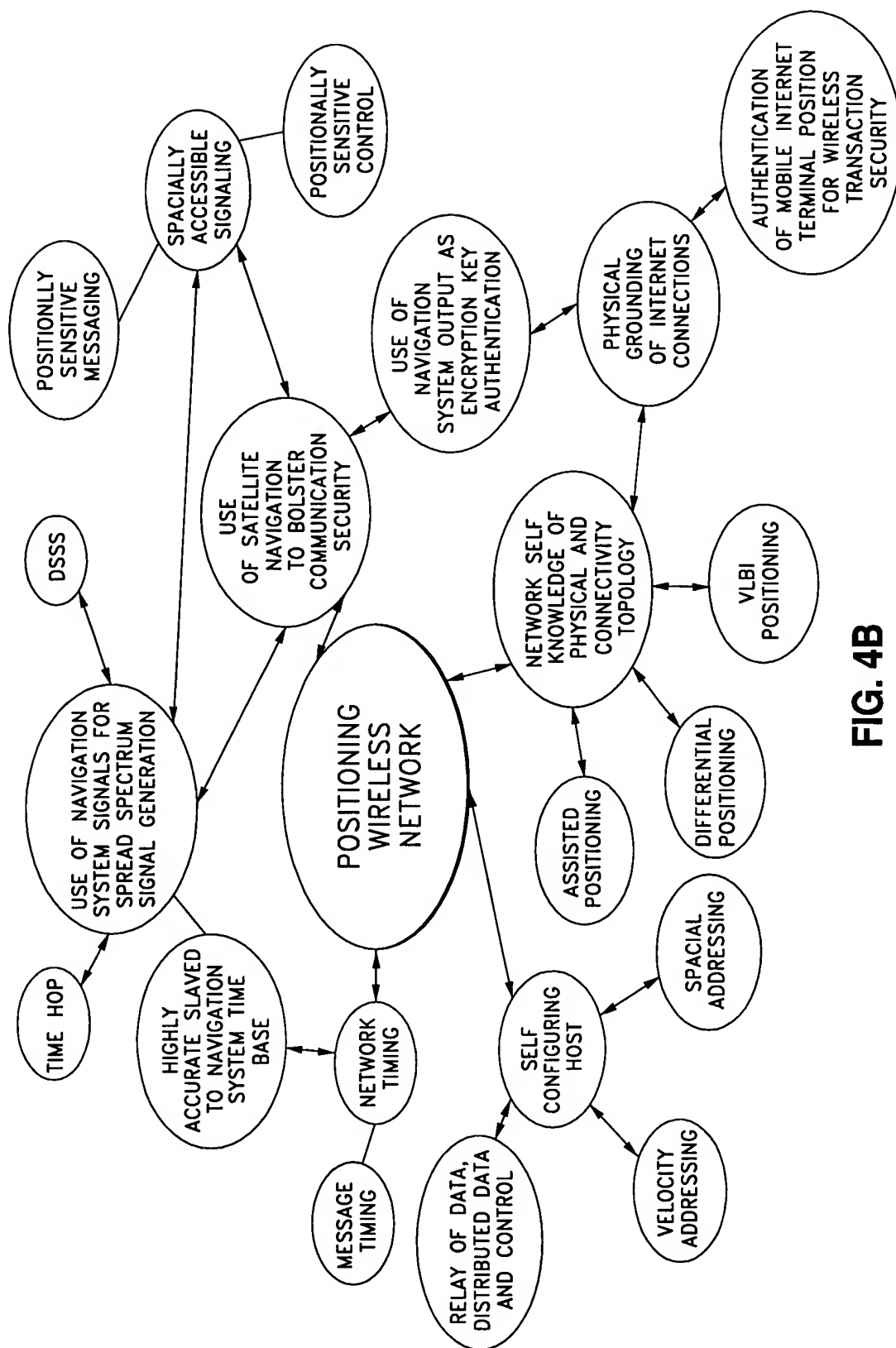


FIG. 4B